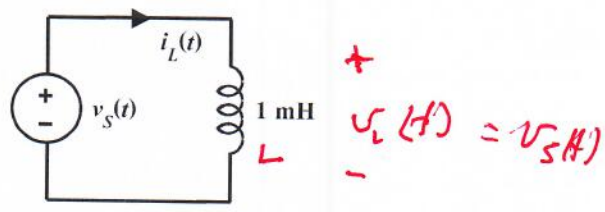
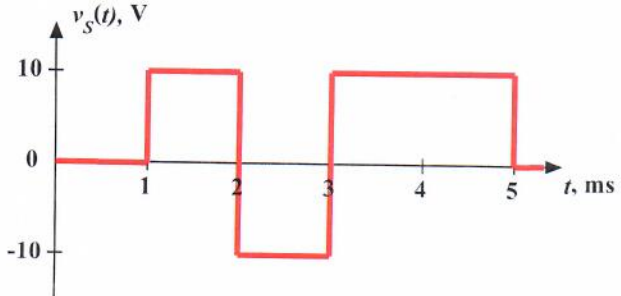


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Homework Problem #043



The independent voltage source is described by the plot shown. Given $i_L(0) = 0\text{ A}$, accurately sketch the waveform for the current $i_L(t)$ for $0 < t$.

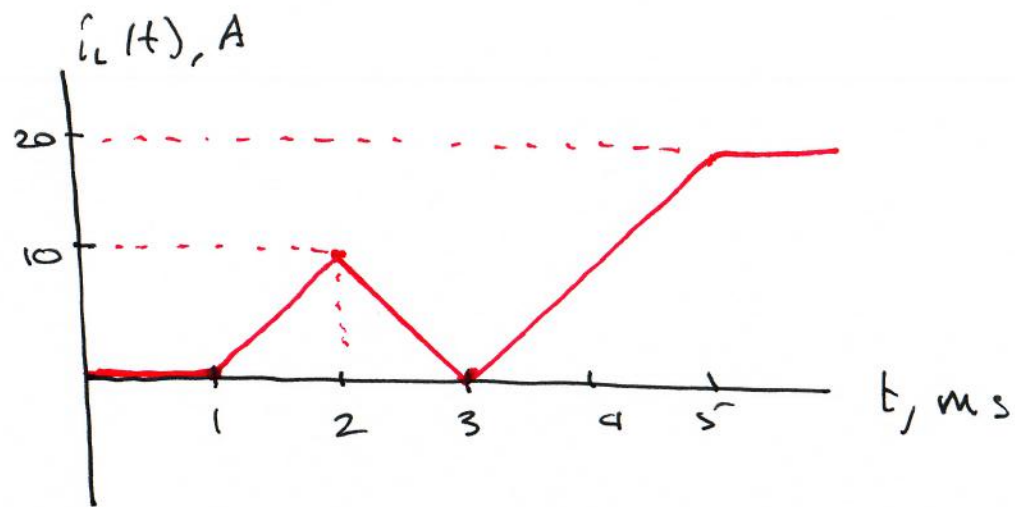
$$i_L(t) = i_L(0) + \frac{1}{L} \int_0^t v(\tau) d\tau = 1000 \int_0^t v(\tau) d\tau$$

$0 < t < 1\text{ ms}$: $v_L(t) = 0 \Rightarrow i_L(t) = 1000 \int_0^t 0 d\tau = 0$

$1\text{ ms} < t < 2\text{ ms}$: $i_L(t) = 0 + 1000 \int_{0.001}^t 10 d\tau = 10000(t - 0.001)$
 $= 0 + 10000t - 10 \text{ A}$

$2\text{ ms} < t < 3\text{ ms}$: $i_L(t) = 0 + 1000 \int_{0.001}^{0.002} 10 d\tau + 1000 \int_{0.002}^t (-10) d\tau$
 $= 0 + 10 - 10000(t - 0.002)$
 $= 30 - 10000t$

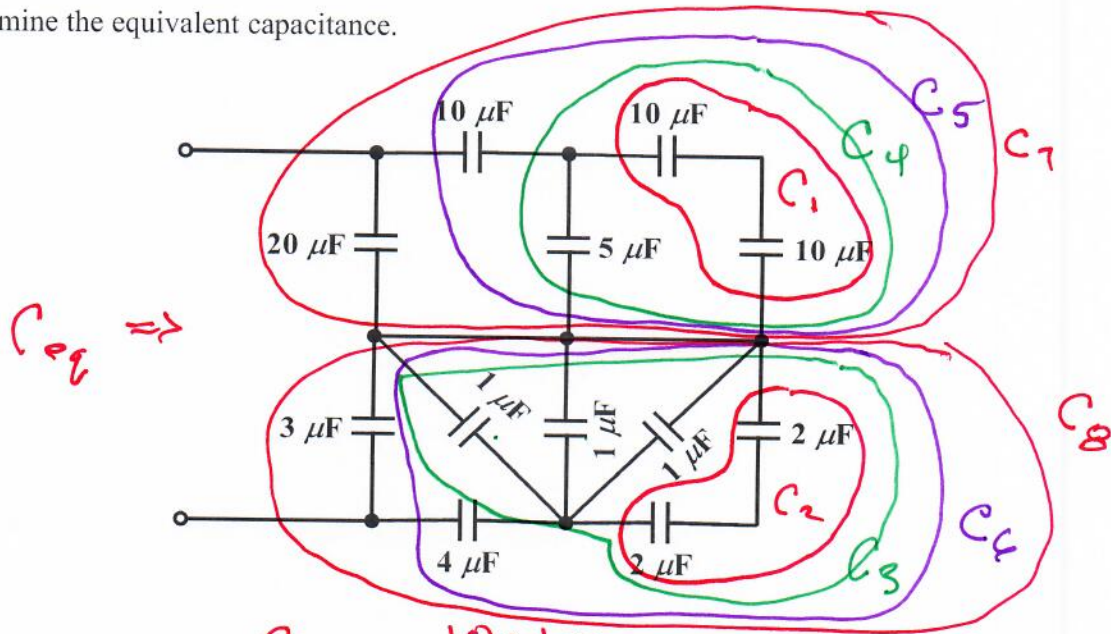
$3\text{ ms} < t < 5\text{ ms}$: $i_L(t) = 30 - 30 + 1000 \int_{0.003}^t 10 d\tau$
 $= 0 + 10000(t - 0.003)$
 $= 10000t - 30$



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Homework Problem #044

Determine the equivalent capacitance.



$$C_1 = \frac{10 \cdot 10}{10 + 10} = 5 \mu\text{F}$$

$$C_2 = \frac{2 \cdot 2}{2 + 2} = 1 \mu\text{F}$$

$$C_3 = 1 + 1 + 1 + C_2 = 4 \mu\text{F}$$

$$C_4 = 5 \mu\text{F} + C_1 = 10 \mu\text{F}$$

$$C_5 = \frac{10 \cdot C_4}{10 + C_4} = 5 \mu\text{F}$$

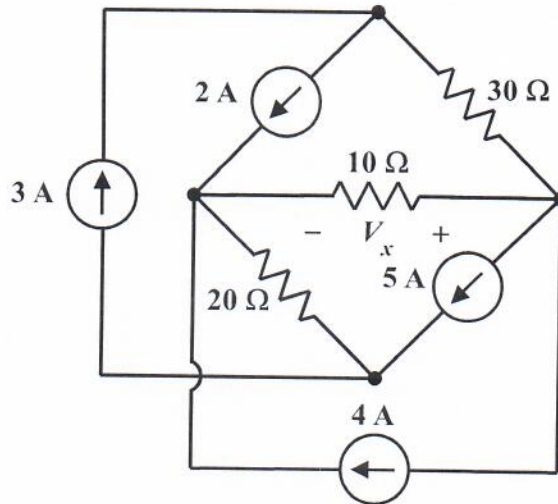
$$C_6 = \frac{4 \cdot 4}{4 + 4} = 2 \mu\text{F}$$

$$C_7 = 20 + C_5 = 25 \mu\text{F}$$

$$C_8 = 3 + C_6 = 5 \mu\text{F}$$

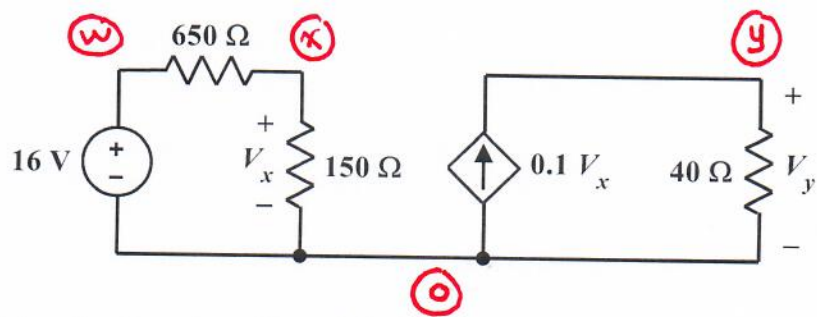
$$C_{eq} = \frac{C_7 \cdot C_8}{C_7 + C_8} = \frac{125}{30} \mu\text{F} = \frac{25}{6} \mu\text{F}$$

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Homework Problem #046



Use PSpice to determine the value of V_x . Submit a complete printed copy of your output file, and highlight the answer in some obvious manner. (Don't forget to put a personal identifier in the title line of your input circuit description file – also called the netlist).

Homework Problem #048



Use PSpice to determine the value of V_y . Submit a complete printed copy of your output file, and highlight the answer in some obvious manner. (Don't forget to put a personal identifier in the title line of your input circuit description file – also called the netlist).

VCCS:

G	0	y	x	0	0.1	Source
↑	↑	↑	↑	↑	↑	coeff.
name	+ node	- node	+ node	- node		

for Vx

assembling PSC